

**AMENDMENTS TO THE CLAIMS**

1. (Cancelled)

2. (Previously Presented) An apparatus for power controlling in a transmitter for receiving an emission signal and outputting to an antenna, comprising:

a plurality of stage amplifiers for receiving the emission signal and amplifying the power thereof;

a plurality of matching circuits connected between the stage amplifiers for matching with the stage amplifiers, respectively;

at least one power detector for detecting the power of the at least one stage amplifier and generating detection signal; and

a bias control circuit for receiving the detection signal of the at least one power detector, thereby generating a bias of each of the stage amplifiers in order to optimize the efficiency of each of the stage amplifiers according to the magnitude of the power of each of the stage amplifiers;

wherein the bias control circuit comprises a comparator, thereby outputting a bypass control signal when the power controller is disabled.

3. (Previously Presented) The apparatus according to claim 2, further comprising a signal bypass unit, thereby grounding the emission signal for preventing the emission signal from being transmitted to each of the stage amplifiers when the bypass control signal is enabled.

4. (Currently Amended) An apparatus of power controlling in a transmitter for receiving an emission signal and outputting to an antenna, comprising:

a plurality of stage amplifiers for receiving the emission signal and amplifying the power thereof;

a plurality of matching circuits connected between the stage amplifiers for matching with the stage amplifiers, respectively;

a plurality of power detectors for detecting the power of the amplifiers and generating detection signals; and

a bias control circuit for receiving the detection signals of the power detectors, thereby generating a bias of each of the stage amplifiers in order to optimize the efficiency of each of the stage amplifiers according to the magnitude of the power of each of the stage amplifiers;

wherein the bias control circuit comprises a comparator, thereby outputting a bypass control signal when the power controller is disabled.

5. (Cancelled)

6. (Currently Amended) The apparatus according to claim ~~54~~, further comprising a signal bypass unit, thereby grounding the emission signal for preventing the emission signal from being transmitted to each of the stage amplifiers when the bypass control signal is enabled.